

10/552601

Terablaze 4

JCD9 Rec'd PCT/PTO 07 OCT 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicants(s): Chung Kuang Chin

Yaw Fann

Roy T. Myers, Jr.

Case: Terablaze 4

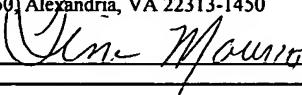
Serial No.: Unassigned

Filing Date: Concurrently Herewith

Group: Unassigned

Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:  Date: October 5, 2005

Title: Method and Apparatus for Shared Multi-Bank Memory

INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying PTO Form 1449.

U.S. Patents , Applications & Publications

1. U.S. Patent No. 5,809,557
2. U.S. Patent No. 5,841,722
3. U.S. Patent No. 5,905,725
4. U.S. Patent No. 5,909,440
5. U.S. Patent No. 5,913,042
6. U.S. Patent No. 5,923,660
7. U.S. Patent No. 5,930,261
8. U.S. Patent No. 5,999,981
9. U.S. Patent No. 6,240,065
10. U.S. Patent No. 6,295,299
11. U.S. Patent No. 6,434,662
12. U.S. Patent No. 6,470,021
13. U.S. Patent Provisional Application Serial No. 60/362,144
14. U.S. Patent Application Publication No. 2002/0169921

A copy of the listed item is enclosed.

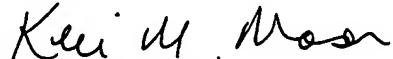
Other Documents

15. Naraghi-Pour et al., "A Multiple Shared Memory Switch," System Theory, IEEE Computer Soc., Proceedings of the Twenty-Eighth Southeastern Symposium on Baton Rouge, LA, pgs. 50-54 (March 31 – April 2, 1996).

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

In the event of any non-payment or improper payment of a required fee, the Commissioner is hereby authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,



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FORM PTO-1449 (MODIFIED)**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT****U.S. PATENT DOCUMENTS**

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE
	5,809,557	10/26/04	Gauthier et al.	327/101	
	5,841,722	11/24/98	Willenz	365/221	
	5,905,725	05/18/99	Sindhu et al.	370/389	
	5,909,440	06/01/99	Ferguson et al.	370/389	
	5,913,042	06/15/99	Shemla et al.	370/429	
	5,923,660	07/13/99	Shemla et al.	370/402	
	5,930,261	07/27/99	Shemla et al.	370/428	
	5,999,981	12/07/99	Willenz et al.	709/238	
	6,240,065	05/29/01	Medina et al.	370/229	
	6,295,299	09/25/01	Haddock et al.	370/423	
	6,434,662	08/31/02	Greene et al.	711/108	
	6,470,021	10/22/02	Daines et al.	370/422	
	60/362,144		Walrand et al.		03/06/02
	2002/0169921	11/14/02	Saitoh	711/5	11/15/01

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

Naraghi-Pour et al., "A Multiple Shared Memory Switch," System Theory, IEEE Computer Soc., Proceedings of the Twenty-Eighth Southeastern Symposium on Baton Rouge, LA, pgs. 50-54 (March 31 – April 2, 1996).

Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.